|  |  |  |
| --- | --- | --- |
| **ALU Select Table** | | |
| **Sel (decimal)** | **Sel (binary)** | **Operation** |
| 0 | 00000 | Add |
| 1 | 00001 | Sub |
| 2 | 00010 | Mult (signed) |
| 3 | 00011 | Mult (unsigned) |
| 4 | 00100 | AND |
| 5 | 00101 | OR |
| 6 | 00110 | XOR |
| 7 | 00111 | LSR (Logical Shift Right) |
| 8 | 01000 | ASR (Arithmetic Shift Right) |
| 9 | 01001 | LSL (Logical Shift Left) |
| 10 | 01010 | ASL (Arithmetic Shift Left) |
| 11 | 01011 | Set on less than (in1 < in2) |
| 12 | 01100 | HI |
| 13 | 01101 | LO |
| 14 | 01110 | Branch if = |
| 15 | 01111 | Branch if != |
| 16 | 10000 | Branch if <= |
| 17 | 10001 | Branch if > 0 |
| 18 | 10010 | Branch if < 0 |
| 19 | 10011 | Branch if >= 0 |